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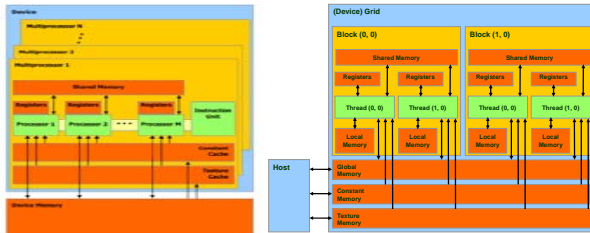
Abstract

- Optical Quadrature Microscopy (OQM) is a technique that is used for imaging of an object by producing interferograms of the sample using different beams of light each having a different phase.
- Affine Transformations carried out on acquired images to adjust orientation of the images.
- Dark signal subtraction done to remove the effects of imperfections in the grabbing mechanism and detector voltages.
- Affine transforms are data parallel operations which can take advantage of massively parallel hardware.
- NVIDIA's **Compute Unified Device Architecture (CUDA)** allows programmers to harness GPU hardware using simple extensions to C.

GPU (Nvidia GeForce 8800 GTX) and CUDA



GeForce 8800



(a) Multiprocessors Architecture (b) Thread and Memory Model
 Figure 1. Architecture of NVIDIA GeForce 80 Series [1]

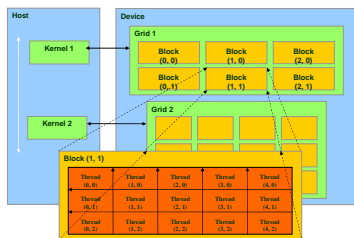
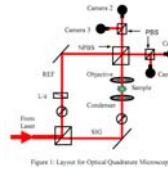


Figure 2. CUDA Thread Batching [1]

Affine Transformation

- Affine Transforms needed in OQM since imaging is done on basis of the phase difference between pixels.
- Used to adjust the grabbed images such that position of each pixel is the same within all 4 images.
- Equations of the Affine Transform are given below,



Camera 0: $E_{0ij}^2 = E_{0i}^2 - 2\text{Re}(E_{0i} E_{0j}^*)$
 Camera 1: $E_{1ij}^2 = E_{1i}^2 - 2\text{Im}(E_{0i} E_{0j}^*)$
 Camera 2: $E_{2ij}^2 = E_{2i}^2 - 2\text{Re}(E_{0i} E_{0j}^*)$
 Camera 3: $E_{3ij}^2 = E_{3i}^2 - 2\text{Im}(E_{0i} E_{0j}^*)$
 $M_{ij} = E_{0i}^2 + E_{0j}^2 - 2\text{Re}(E_{0i}^* E_{0j})$

Consider a pixel at (x,y) . If it is rotated by θ
 The new pixel position is given by (x',y')

Rotation Matrix

$$\begin{pmatrix} x' \\ y' \end{pmatrix} = \begin{pmatrix} \cos(\theta) & -\sin(\theta) \\ \sin(\theta) & \cos(\theta) \end{pmatrix} \begin{pmatrix} x \\ y \end{pmatrix}$$

Shearing Matrices, if the shearing is defined by $sh(x)$ or $sh(y)$

Shearing Matrices

$$\begin{pmatrix} x' \\ y' \end{pmatrix} = \begin{pmatrix} 1 & sh(y) \\ 0 & 1 \end{pmatrix} \begin{pmatrix} x \\ y \end{pmatrix}$$

$$\begin{pmatrix} x' \\ y' \end{pmatrix} = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix} \begin{pmatrix} x \\ y \end{pmatrix}$$

Combining rotation & shearing along with matrix scaling and displacement we get

Affine Transform Matrix

$$\begin{pmatrix} x' \\ y' \end{pmatrix} = \begin{pmatrix} s(x) \cos(\theta) & sh(y) \sin(\theta) \\ -sh(x) \sin(\theta) & s(y) \cos(\theta) \end{pmatrix} \begin{pmatrix} x \\ y \end{pmatrix} + \begin{pmatrix} a \\ b \end{pmatrix}$$

Affine Transformation on CUDA

- Each thread works on one Pixel,
- Groups of threads (working on groups of pixels) are separated into blocks. A block is executed on one multiprocessor on the GPU.
- Algorithm for Each Pixel located at (x,y) – Repeated for all threads
 - Load Affine Matrix $f()$
 - Calculate location of pixel (x,y) (By looking at block & Thread id)
 - New Position $(x1,y1) = (f(x),f(y))$
 - Store Pixel value in (x,y) in $(f(x),f(y))$

Last step causes memory bottlenecks since accesses are not aligned.

Noise Subtraction on CUDA

- Fixed pattern levels not removed by common mode rejection (beam splitters) or dark subtraction since they are different for each camera.
- Requires the subtraction of S_n and R_n from Mixed Image.
- Implemented on GPU using **CUBLAS** (BLAS Implementation for CUDA)

$$E = \frac{1}{4} \sum_{n=0}^3 i^n * \frac{M_n - S_n - R_n}{\sqrt{R_n}}$$

- Angle of this complex number is the phase information for the sample.

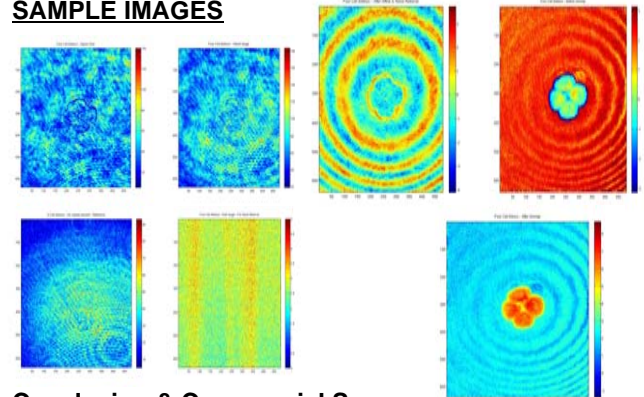
Performance Improvements

- Serial Version in MATLAB: **1.5 seconds**
- CUDA Version: NVIDIA G8800 GTX = **0.2seconds ~ 8X Speedup for heavily memory bound workload.**
- Improvement in GPU based phase unwrap code due to removal of disk activity = **2.8sec to 2sec ~ 30% less time**

Matlab External Interface (MEX)

- GPU code needs to be accessible from within MATLAB.
- MEX files are dynamically linked libraries produced by MATLAB, allowing us to call C code within MATLAB.
- Argument passing between MATLAB and C uses the mxArray which is a C structure used by MATLAB to store data is made accessible to our code using pointer accesses.
- Using MEX wrappers we combine frame-grabbing, Affine Transforms, Noise Removal and Phase Unwrap into one automated system.

SAMPLE IMAGES



Conclusion & Commercial Scope

- Affine Transform algorithm has been implemented on the GPU.
- Using MEX has helped to remove unnecessary disk IO from the critical path and regular use case.
- MEX yields performance improvements not only in the Affine Transform but also in the phase unwrap part of this project as well.
- Optimizing memory access, availability of asynchronous memory transfer and processing on the GPU would yield further improvements.
- Thus by implementing Affine Transforms and Noise removal on CUDA and by integrating the different components of our system, we get closer to the Optical Science Lab's goal of real time OQM imaging.

References

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- Master's Thesis – Sherman Braganza
- NVIDIA CUBLAS Manual

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